



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the application of:

KONNO et al

RECEIVED

Serial No.: 07/743,383 JUL 08 1998 Group Art Unit: 1104

Filed: August 21, 1991 GROUP 2100 Examiner: G. Goudreau

For: METHOD FOR PRODUCING SEMICONDUCTOR INTEGRATED CIRCUITS AND  
APPARATUS USED IN SUCH METHOD

PETITION FOR EXTENSION OF TIME

Honorable Commissioner  
of Patents and Trademarks  
Washington, D.C. 20006

January 7, 1993

Sir:

Applicant petitions the Commissioner of Patents and Trademarks to extend the time for response to the Office Action dated October 5, 1992, for one month from January 5, 1993, to February 5, 1993.

Please charge Deposit Account Number 14-1060 in the amount of One Hundred and Ten Dollars (\$110.00) to cover the cost of the extension. In the event that any additional fees are due with respect to this paper, please charge our Deposit Account No. 14-1060. A duplicate copy of this paper is enclosed herewith.

Respectfully submitted,

Juan Carlos A. Marquez  
Attorney for Applicants  
Registration No. 34,072

Atty. Docket No. P698-1333

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Enclosure: Duplicate copy of Petition